

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application. The following listing provides the amended claims with the amendments marked with deleted material crossed out and new material underlined to show the changes made.

1. (Currently Amended) An integrated-circuit ("IC") layout comprising:
 - a) a net with routable elements;
 - b) a first set of interconnect lines for connecting the routable elements of the ~~net-nets~~, wherein the interconnect lines have ends that are in the shape of a partial non-quadrilateral polygons.
2. (Original) The IC layout of claim1, wherein the partial polygon is a partial octagon.
3. (Original) The IC layout of claim 2, wherein the partial octagon has several sides that are equal.
4. (Original) The IC layout of claim 3, wherein the partial octagons are half-octagons.
5. (Original) The IC layout of claim 1, wherein the partial polygon is a partial hexagon.
6. (Original) The IC layout of claim 5, wherein the partial hexagon has several equal sides.
7. (Original) The IC layout of claim 5, wherein the partial hexagons are half hexagons.

8. (Currently Amended) The IC layout of claim 1 further comprising a first set of vias that are in the shape of the non-quadrilateral polygon.

9. (Original) The IC layout of claim 8, wherein the polygon is an octagon.

10. (Original) The IC layout of claim 9, wherein the octagon has several sides that are equal.

11. (Original) The IC layout of claim 10, wherein the partial octagon ends of the interconnect lines are half octagons.

12. (Original) The IC layout of claim 8, wherein the polygon is a hexagon.

13. (Original) The IC layout of claim 12, wherein the hexagon has several equal sides.

14. (Original) The IC layout of claim 13, wherein the partial hexagon ends of the interconnect lines are half hexagons.

15. (Currently Amended) The IC layout of claim 1 further comprising a first set of vias that are in the shape of another non-quadrilateral polygon.

16. (Currently Amended) The IC layout of claim 1 further comprising a second set of interconnect lines that have end that are in the shape of a partial quadrilateral polygons.

17. (Original) The IC layout of claim 16, wherein the second set of interconnect lines have partial rectangular ends.

18. (Currently Amended) The IC layout of claim 16 further comprising a second set of vias that are in the shape of a quadrilateral polygon.